

AMENDMENTS TO THE SPECIFICATION

Page 10, beginning with line 21, amend the paragraph as follows.

FIG. 2 is a circuit diagram showing a configuration of the latch circuit 60, an example of which is a holding circuit. As shown in FIG. 2, the latch circuit 60 has two inverters 61, 62. The input terminal of the inverter 61 is connected to the output terminal of the inverter 62. The output terminal of the inverter 61 is connected to the input terminal of the inverter 62. The connection node of the input terminal of the inverter 61 and the output terminal of the inverter 62 is connected to a write global bit line. The power supply voltage for the inverters 61, 62 is VBB (for example, -8 V) and Vcc (for example, 1.5 V) or GND. Whether the high-voltage side of the power supply voltage is Vcc or GND is determined by a switch element 63.